

FIG. 1.

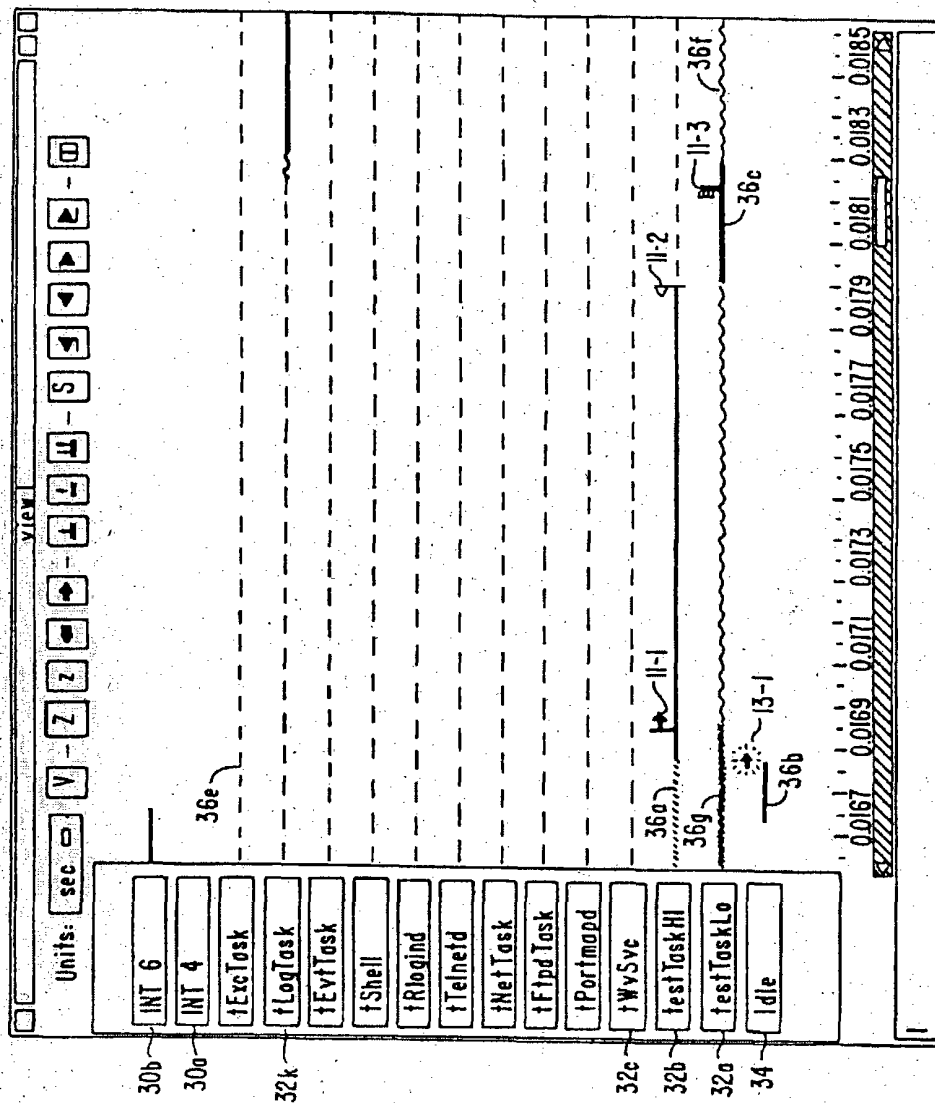


FIG. 2

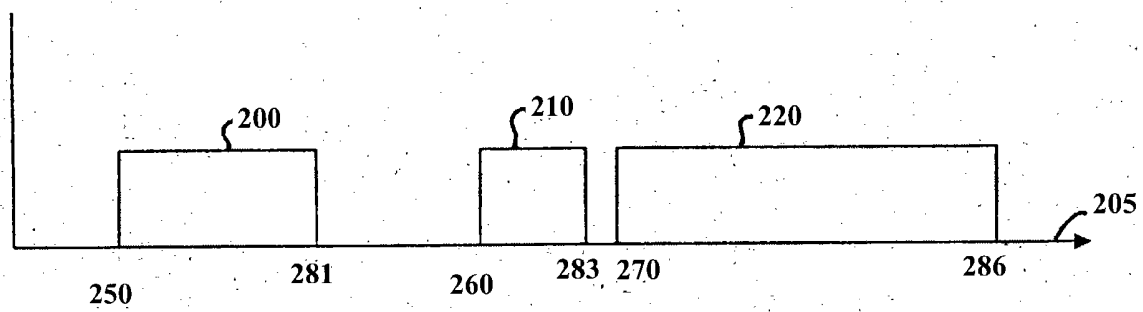


Figure 3

310	DISPATCHER ROUND OF 30 MS	INTERUPT ENABLE TIME	DEADLINE (WORST CASE)	
	320	330 (MINIMUM TIME BETWEEN SUCCESSIVE INTERRUPTS)	340	
Process	Time			
Task 1 322	0		20	
Task 2 324	10		30	
Task 3 326	25		60	
Idle				
ISR 1 332		4		
ISR 1 332		8		
....		...		
ISR 1 332		28		
ISR 2 334		14		

Figure 4A

310	DISPATCHER ROUND OF 30 MS	INTERUPT ENABLE TIME	DEADLINE (WORST CASE)	
	320	330 (MINIMUM TIME BETWEEN SUCCESSIVE INTERRUPTS)	340	
Process	Time			
Task 1 322	1		8	
Task 2 324	10		20	
Task 3 326	15		30	
Idle				
ISR 1 332		4		
ISR 1 332		8		
....		...		
ISR 1 332		28		
ISR 2 334				

Figure 4B

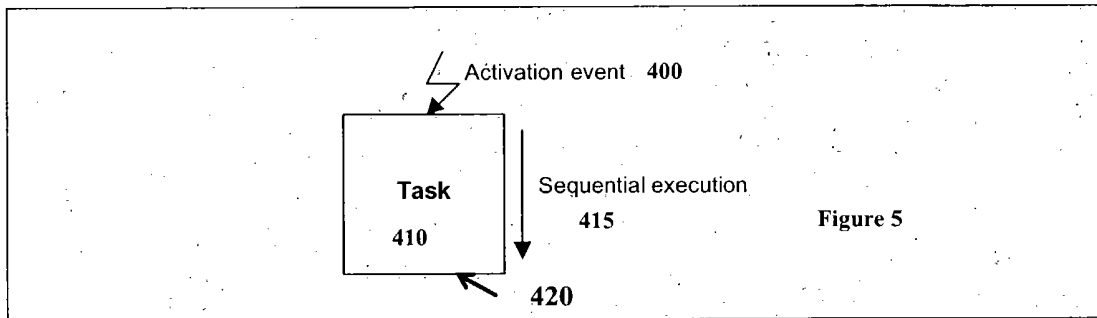


Figure 5

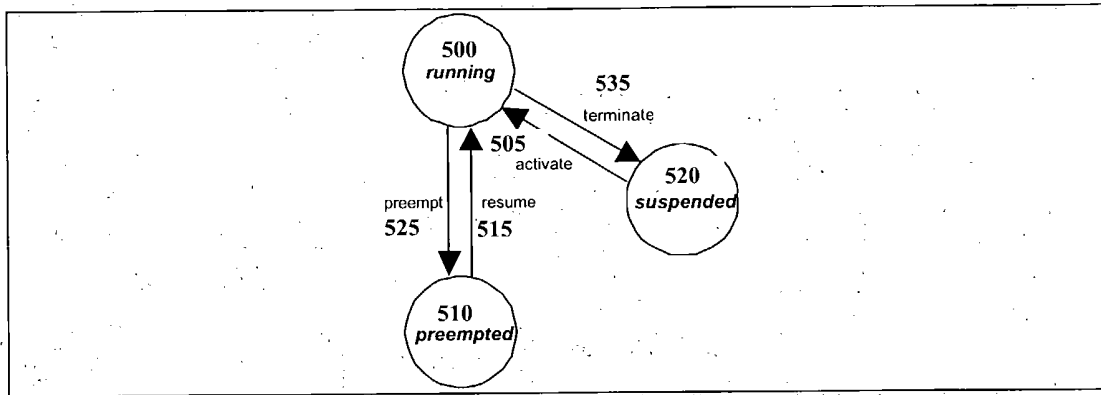


Figure 6

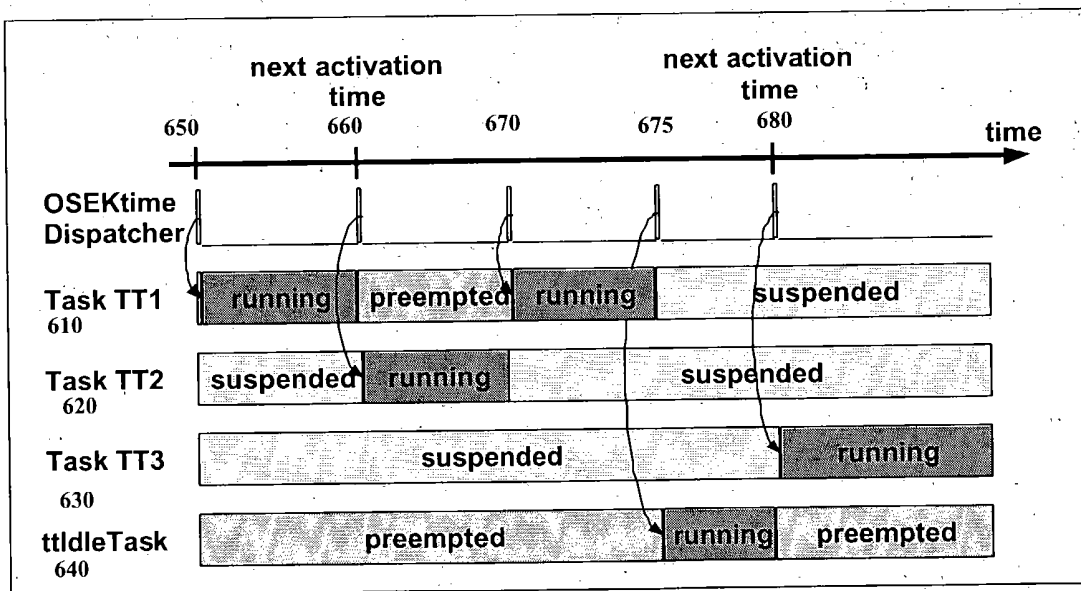


Figure 7

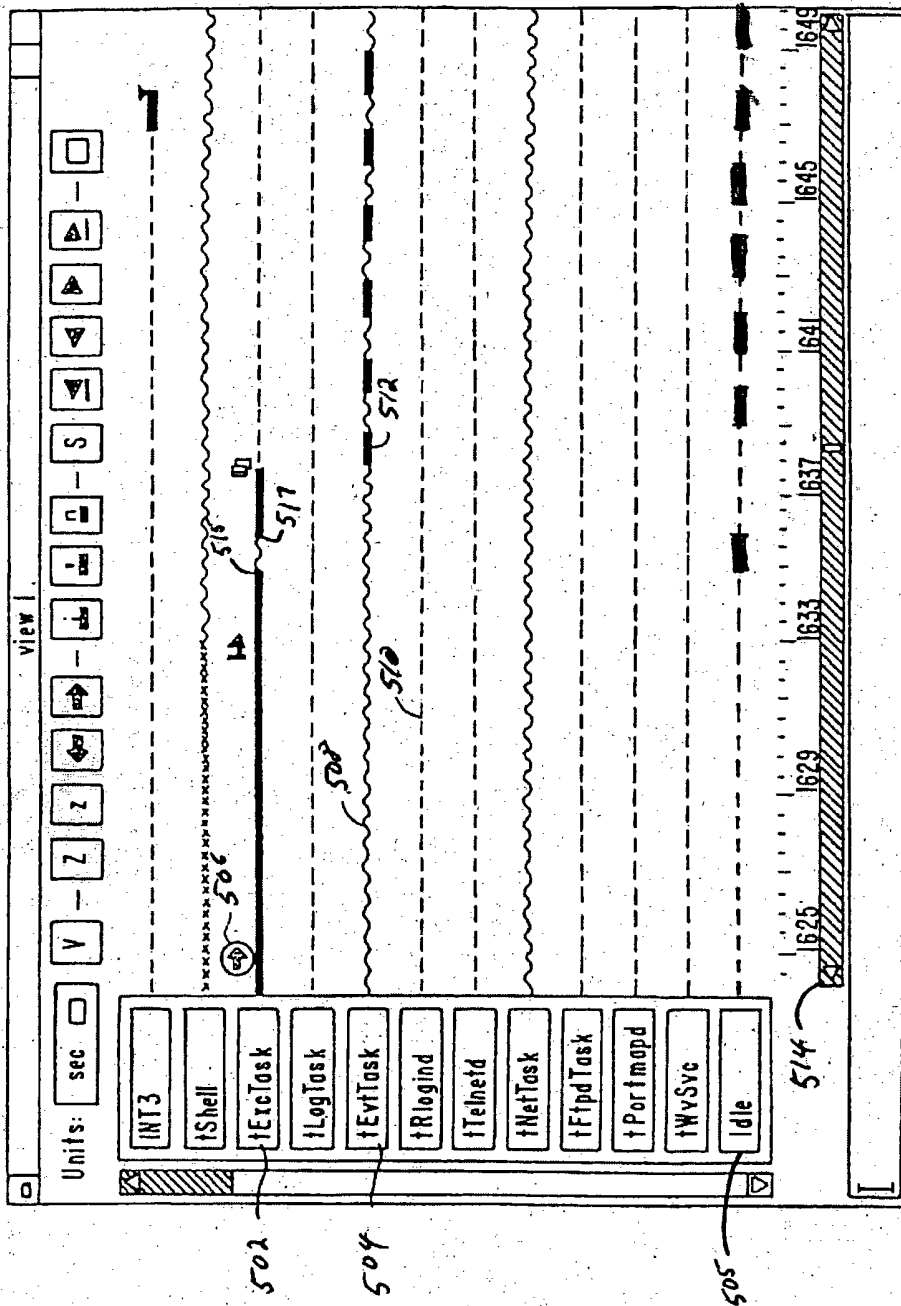


Figure 8A

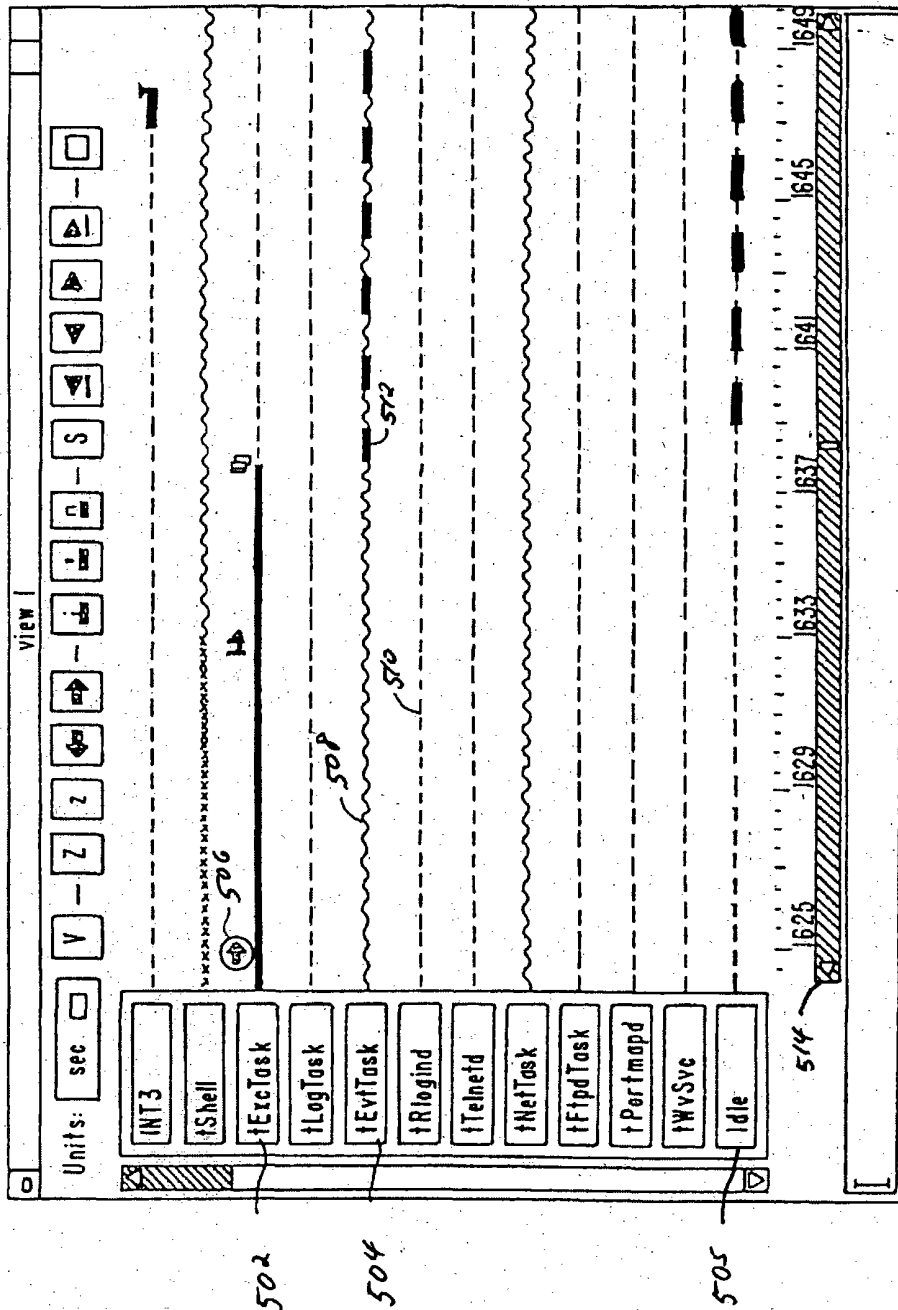


Figure 8B

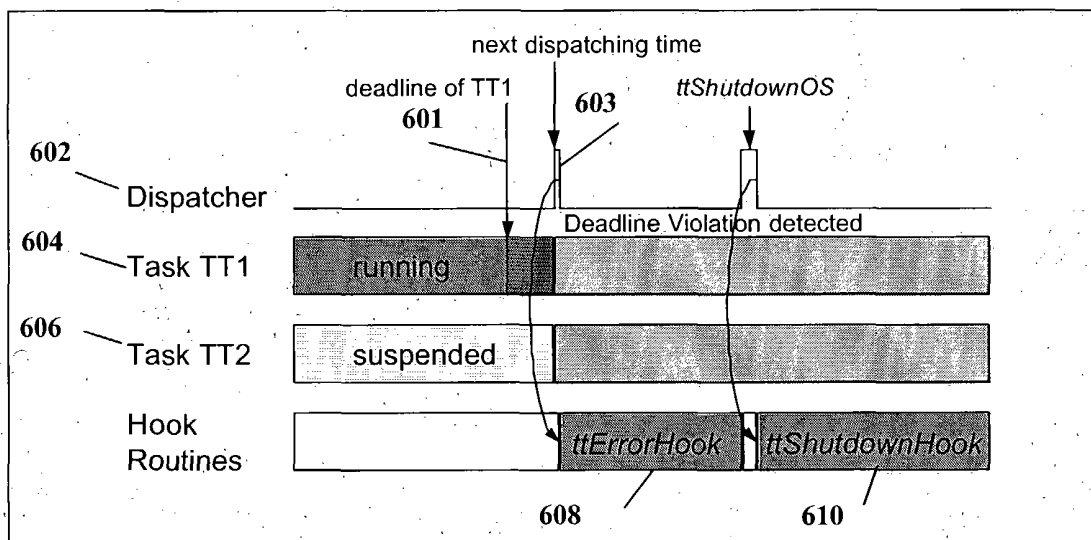
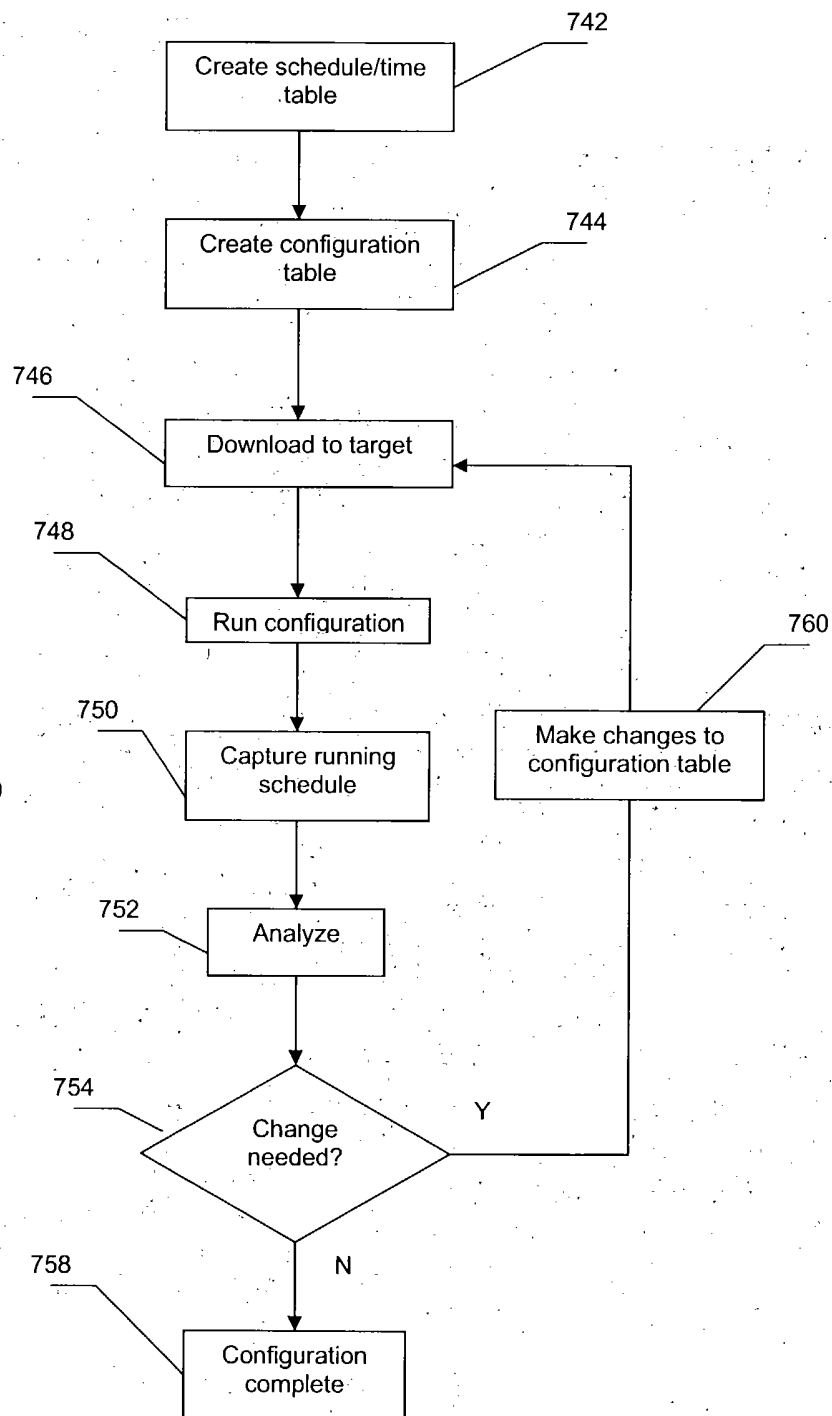


Figure 9

Figure 10



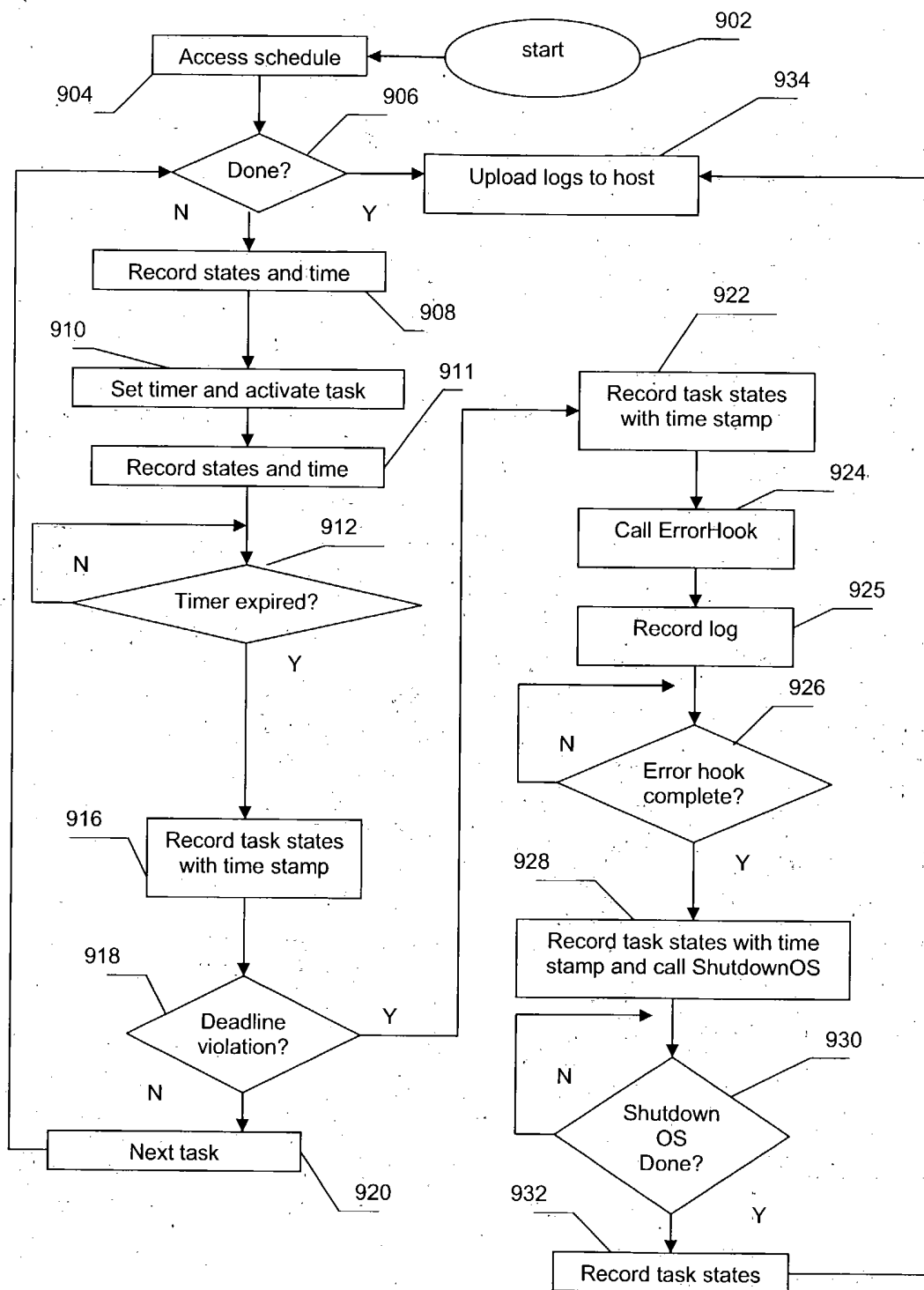


Fig. 11